UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,980,233 B1

Page 1 of 19

APPLICATION NO.: 09/348500

: December 27, 2005

INVENTOR(S)

: Masahide Hirasawa

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE DRAWINGS:

Delete drawing sheets 1-19 and 33, and subtitute therefor, drawing sheets 1-19 and 33. (Attached)

COLUMN 2:

Line 29, "PCes" should read --PCs--.

Line 33, "a" should be deleted.

Line 35, "is" should read -- are--.

COLUMN 6:

Line 35, "cross sectional" should read --cross-sectional--.

COLUMN 9:

Line 30, "cross sectional" should read --cross-sectional--.

COLUMN 13:

Line 65, "IDes" should read -- IDs--.

COLUMN 14:

Line 11, "IDes" (both occurrences) should read -- IDs--.

COLUMN 16:

Line 11, "IDes" should read -- IDs--.

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Page 2 of 19

DATED

APPLICATION NO. : 09/348500

INVENTOR(S)

: December 27, 2005 : Masahide Hirasawa

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 26:

Line 33, "are" should read --is--.

Signed and Sealed this

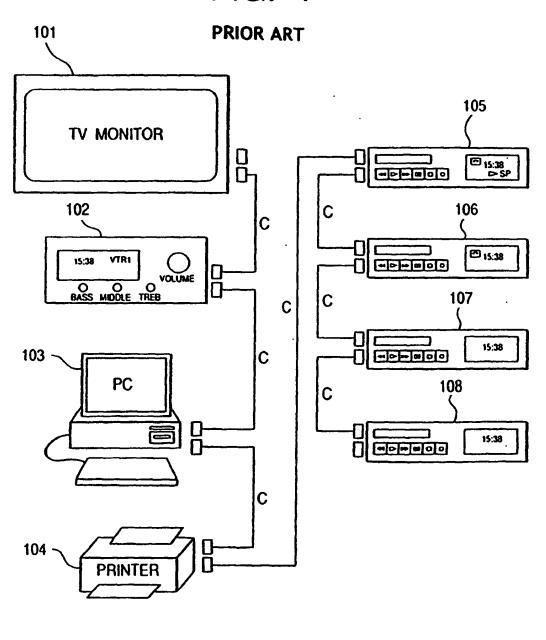
Seventeenth Day of October, 2006

JON W. DUDAS
Director of the United States Patent and Trademark Office

Dec. 27, 2005

Sheet 1 of 30

FIG. 1



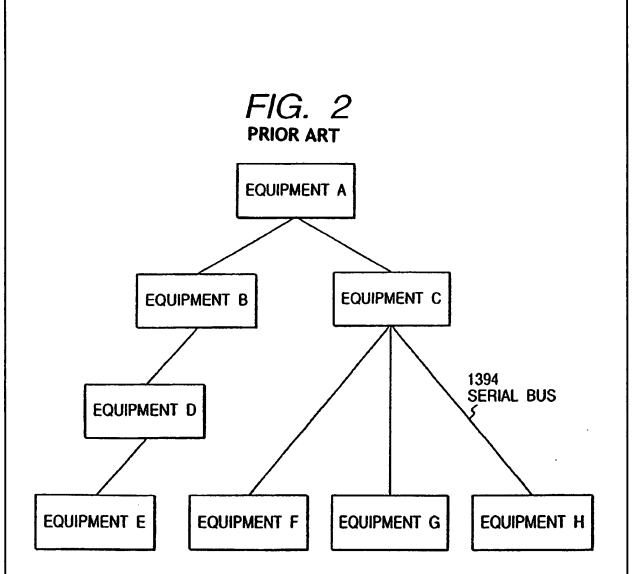
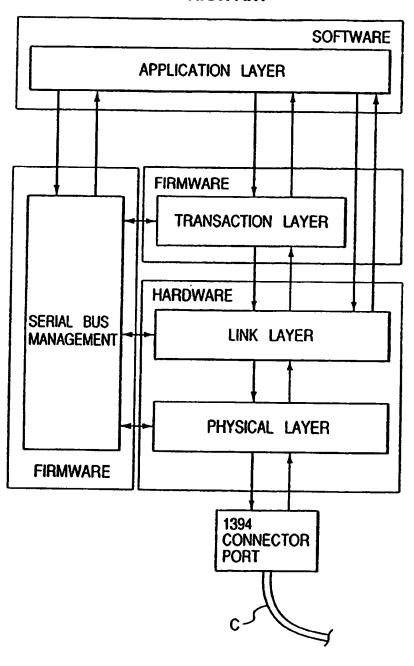


FIG. 3 **PRIOR ART**



U.S. Patent

Dec. 27, 2005

Sheet 5 of 30

6,980,233 B1

FIG. 5

PRIOR ART

ELECTRIC POWER SUPPLY CONDUCTOR (8-40V MAX. DC CURRENT 1.5A)

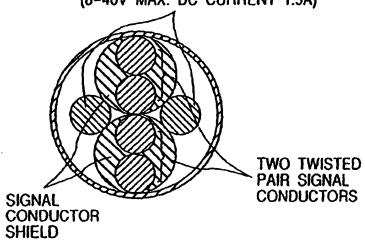
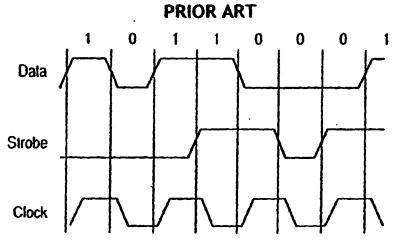
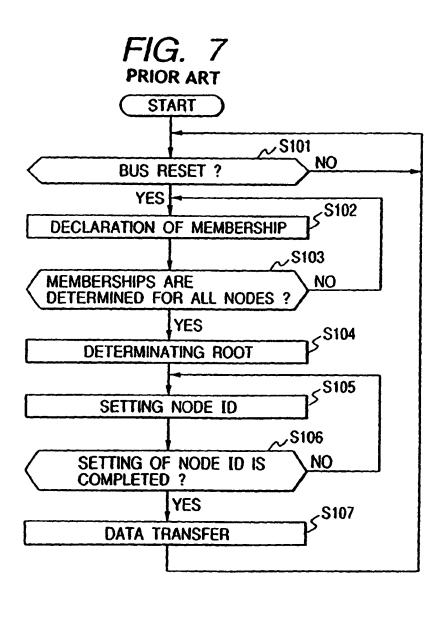


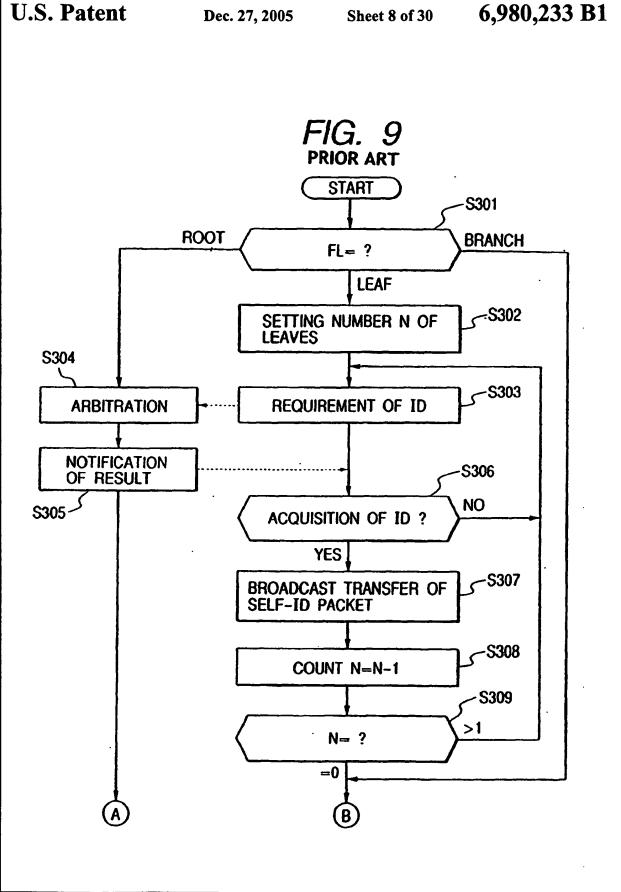
FIG. 6

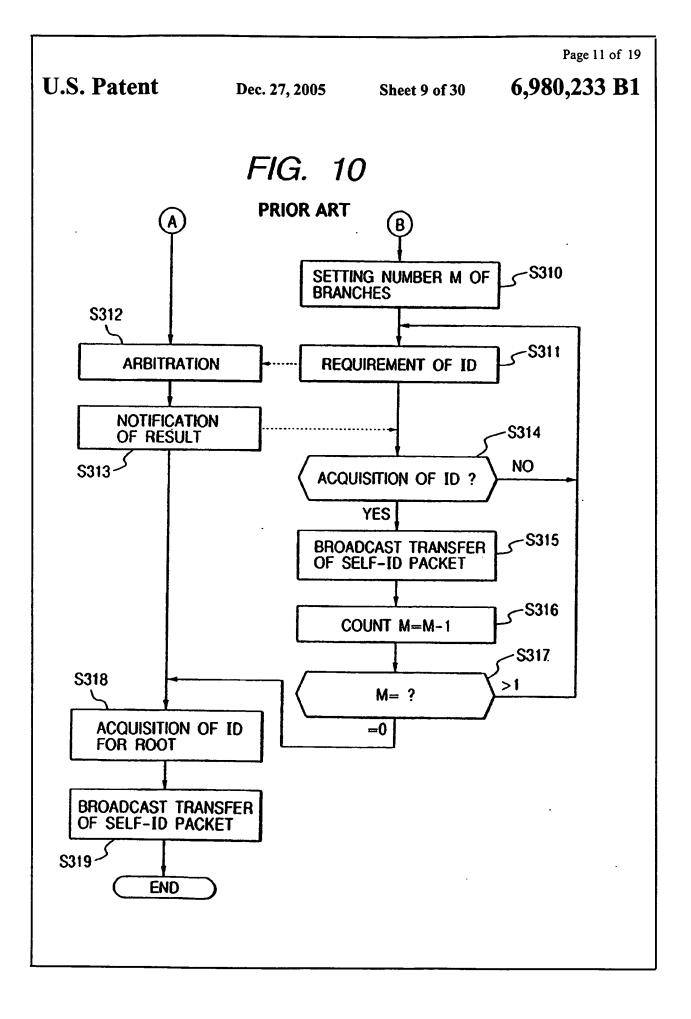


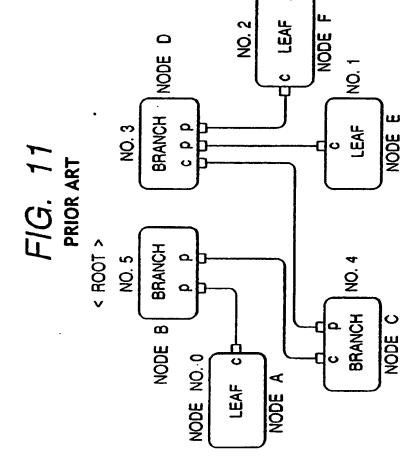
(SIGNAL OF EXCLUSIVE OR BETWEEN Data AND Strobe)

Dec. 27, 2005 Sheet 6 of 30

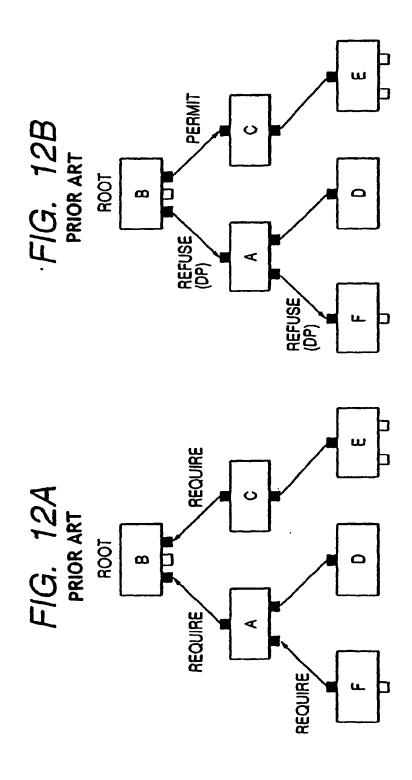


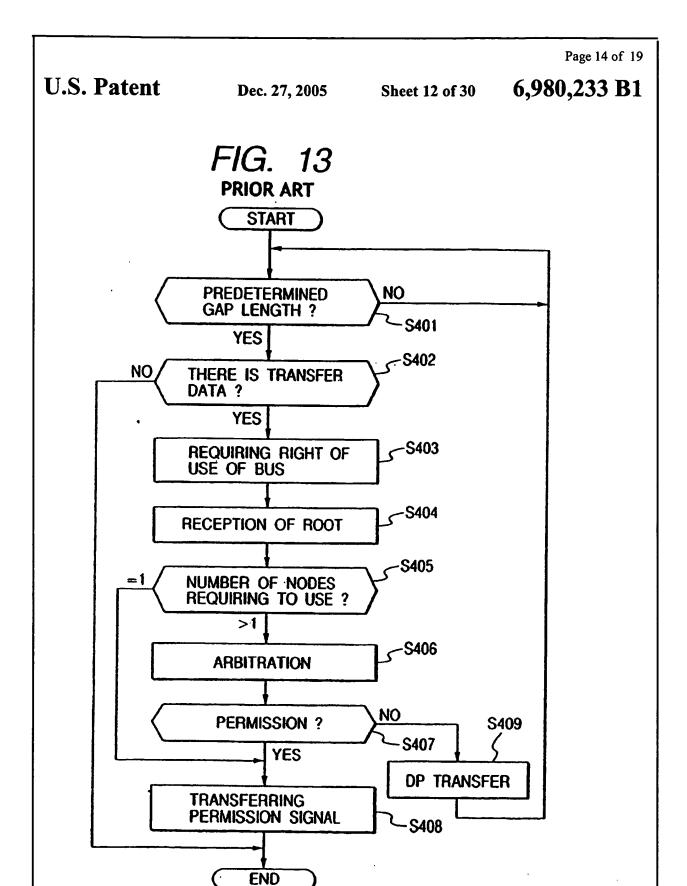


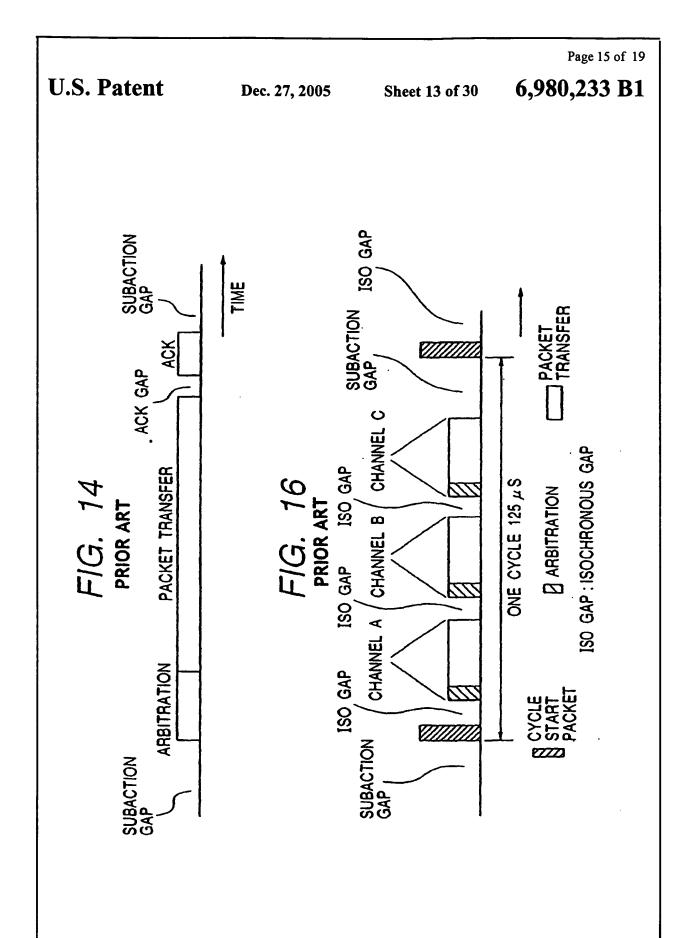




BRANCH: NODE HAVING MORE THAN TWO NODE CONNECTIONS
LEAF: NODE HAVING CONNECTION WITH ONLY ONE PORT
c: PORT CORRESPONDING TO NODE OF CHILD
p: PORT CORRESPONDING TO NODE OF PARENT

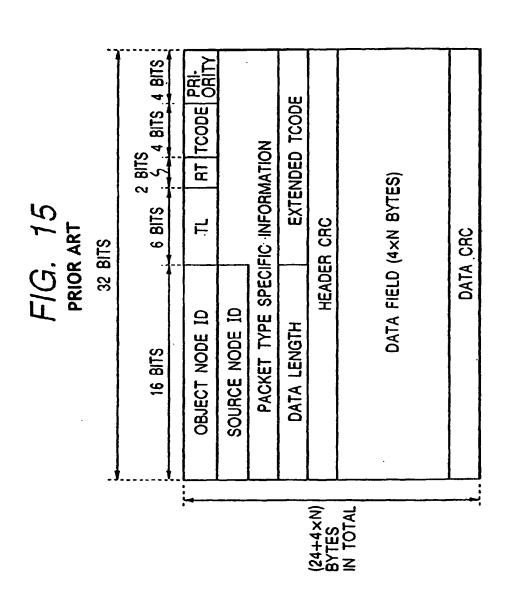






Dec. 27, 2005

Sheet 14 of 30



U.S. Patent

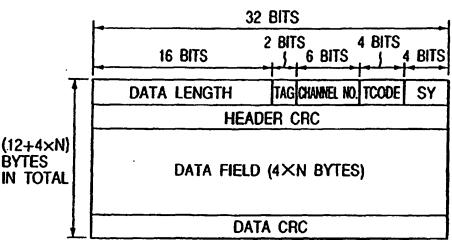
Dec. 27, 2005

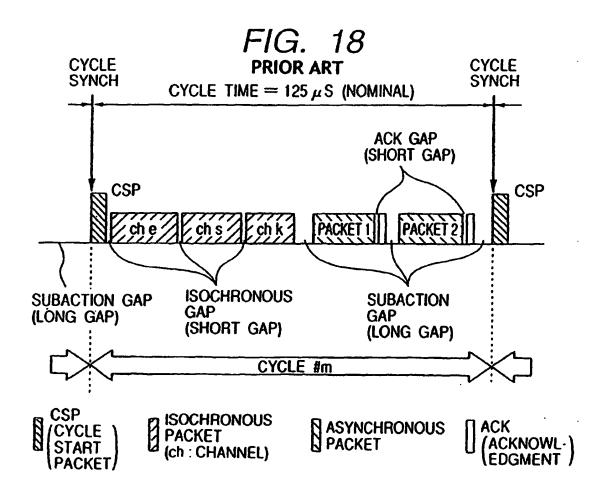
Sheet 15 of 30

6,980,233 B1

FIG. 17

PRIOR ART





Dec. 27, 2005

Sheet 30 of 30

